| L Number | Hits | Search Text | DB | Time stamp |
|-------------|-------|---|---|---------------------|
| 1 | 278 | ((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM) | USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 16:25 |
| 2 | 23 | (((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls. | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/10/14 16:25 |
| 4 | 9441 | ((sram or (static adj6 random adj5 access adj5 memory)) and "memory device") and transistor | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/10/14 16:26 |
| 5 | 3 | (((sram or (static adj6 random adj5 access adj5 memory)) and "memory device") and transistor) and ("four\$1transistor" or 4T\$1SRAM) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/10/14 16:26 |
| 6 | 83 | <pre>(((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and ((((sram or (static adj5 random adj5 access adj6 memory)) and</pre> | USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 16:27 |
| 7 | 147 | transistor) and ("four-transistor" or 4T-SRAM)) and 365/\$7.ccls.) (((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and | USPAT; US-PGPUB; EPO; JPO; | 2004/10/14 |
| 8 | 2 | 365/\$7.ccls. | DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 |
| 9 | 23 | (((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls. | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 16:28 |
| 10 | 51 | (((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/154.ccls. | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 16:28 |
| 11 | 2 | (((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls. | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 16:28 |
| - | 47057 | sram or (static adj5 random adj5 access adj5 memory) | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/06/01 01:46 |
| - | 19882 | (sram or (static adj5 random adj5 access adj5 memory)) and transistor | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/06/01 01:46 |
| - | 25,8 | ((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM) | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/10/14 16:25 |

| - | 21 | (((sram or (static adj5 random adj5 | USPAT; | 2004/10/14 |
|--------------|-------|--|-----------|------------|
| | | access adj5 memory)) and transistor) and | US-PGPUB; | 16:25 |
| | | ("four-transistor" or 4T-SRAM)) and | EPO; JPO; | i e |
| | | 365/156.ccls. | DERWENT; | |
| | | | IBM TDB | |
| _ | 16318 | (sram or (static adj5 random adj5 access | USPAT; | 2004/10/14 |
| | | adj5 memory)) and "memory device" | US-PGPUB; | 16:26 |
| | | 1,, | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM TDB | |
| _ | 8656 | ((sram or (static adj5 random adj5 access | USPAT; | 2004/10/14 |
| - | 0030 | adj5 memory)) and "memory device") and | US-PGPUB; | 16:26 |
| | | transistor | EPO; JPO; | 10.20 |
| | | transistor | | |
| | | | DERWENT; | |
| | _ | | IBM_TDB | 0004/10/14 |
| - | 3 | (((sram or (static adj5 random adj5 | USPAT; | 2004/10/14 |
| | | access adj5 memory)) and "memory device") | US-PGPUB; | 16:26 |
| | | and transistor) and ("four\$1transistor" | EPO; JPO; | |
| | | or 4T\$1SRAM) | DERWENT; | 1 |
| | | | IBM_TDB | |
| _ | 3 | (((sram or (static adj5 random adj5 | USPAT; | 2004/06/01 |
| | | access adj5 memory)) and "memory device") and transistor) and 4T\$1SRAM | US-PGPUB; | 01:54 |
| | | | EPO; JPO; | |
| | | , | DERWENT; | |
| | | | IBM TDB | |
| _ | 75 | (((sram or (static adj5 random adj5 | USPAT; | 2004/10/14 |
| "" , | '3 | access adj5 memory)) and "memory device") | US-PGPUB; | 16:27 |
| | | | | 10.27 |
| | | and transistor) and ((((sram or (static | EPO; JPO; | |
| | | adj5 random adj5 access adj5 memory)) and | DERWENT; | |
| | 1 | transistor) and ("four-transistor" or | IBM_TDB | |
| | | 4T-SRAM)) and 365/\$7.ccls.) | | |
| - | 0 | chou-chung.in. | USPAT; | 2004/06/01 |
| | | | US-PGPUB; | 01:55 |
| | 1 | | EPO; JPO; | |
| | | | DERWENT; | 1 |
| | | | IBM TDB | |
| - | 29 | chou-chung-\$.in. | USPAT; | 2004/06/01 |
| | | | US-PGPUB; | 01:55 |
| | - | | EPO; JPO; | |
| | | | DERWENT; |] |
| | | | IBM TDB | |
| | 2 | chung-chou.in. | USPAT; | 2004/06/01 |
| - | | Chang-choa.in. | US-PGPUB; | |
| | 1 | | 1 | 01:55 |
| | 1 | | EPO; JPO; | 1 |
| | | | DERWENT; | |
| | _ | | IBM_TDB | 0004/05/05 |
| - | 0 | chung-chou-\$.in. | USPAT; | 2004/06/01 |
| | | | US-PGPUB; | 01:55 |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | 1 | | IBM_TDB | |
| _ | 0 | chou-chung-c.in. | USPAT; | 2004/06/01 |
| | | | US-PGPUB; | 01:55 |
| | | | EPO; JPO; | |
| | 1 | | DERWENT; | |
| | 1 | | IBM TDB | |
| _ | 12 | "four\$1transistor" or 4T\$1SRAM | USPAT; | 2004/06/01 |
| | | 200271010101001 01 119101011 | US-PGPUB; | 01:57 |
| | | | EPO; JPO; | 1 32.37 |
| | | | DERWENT; | |
| | | | | |
| I _ | 137 | ///gram or /gtatic adit was des | IBM_TDB | 2004/10/14 |
| I - | 13/ | (((sram or (static adj5 random adj5 | USPAT; | 2004/10/14 |
| | | access adj5 memory)) and transistor) and | US-PGPUB; | 16:27 |
| | | ("four-transistor" or 4T-SRAM)) and | EPO; JPO; | |
| | | 365/\$7.ccls. | DERWENT; | |
| | | | IBM_TDB | 1 |
| - | 2 | (((| USPAT; | 2004/10/14 |
| | ı | access adj5 memory)) and transistor) and | US-PGPUB; | 16:27 |
| 1 |] | ("four-transistor" or 4T-SRAM)) and | EPO; JPO; | |
| | | 365/191.ccls. | DERWENT; | |
| | | | IBM TDB | |
| <u> </u> | | - · · · · · · · · · · · · · · · · · · · | | <u> </u> |

| _ | 4 | (((sram or (static adj5 random adj5 access adj5 memory)) and transistor) ("four-transistor" or 4T-SRAM)) and 365/226.ccls. | and | USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/10/14 16:27 |
|---|----|--|-----|--|---------------------|
| _ | 48 | (((sram or (static adj5 random adj5 access adj5 memory)) and transistor) ("four-transistor" or 4T-SRAM)) and 365/154.ccls. | and | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/06/01 |